

Fig. 2

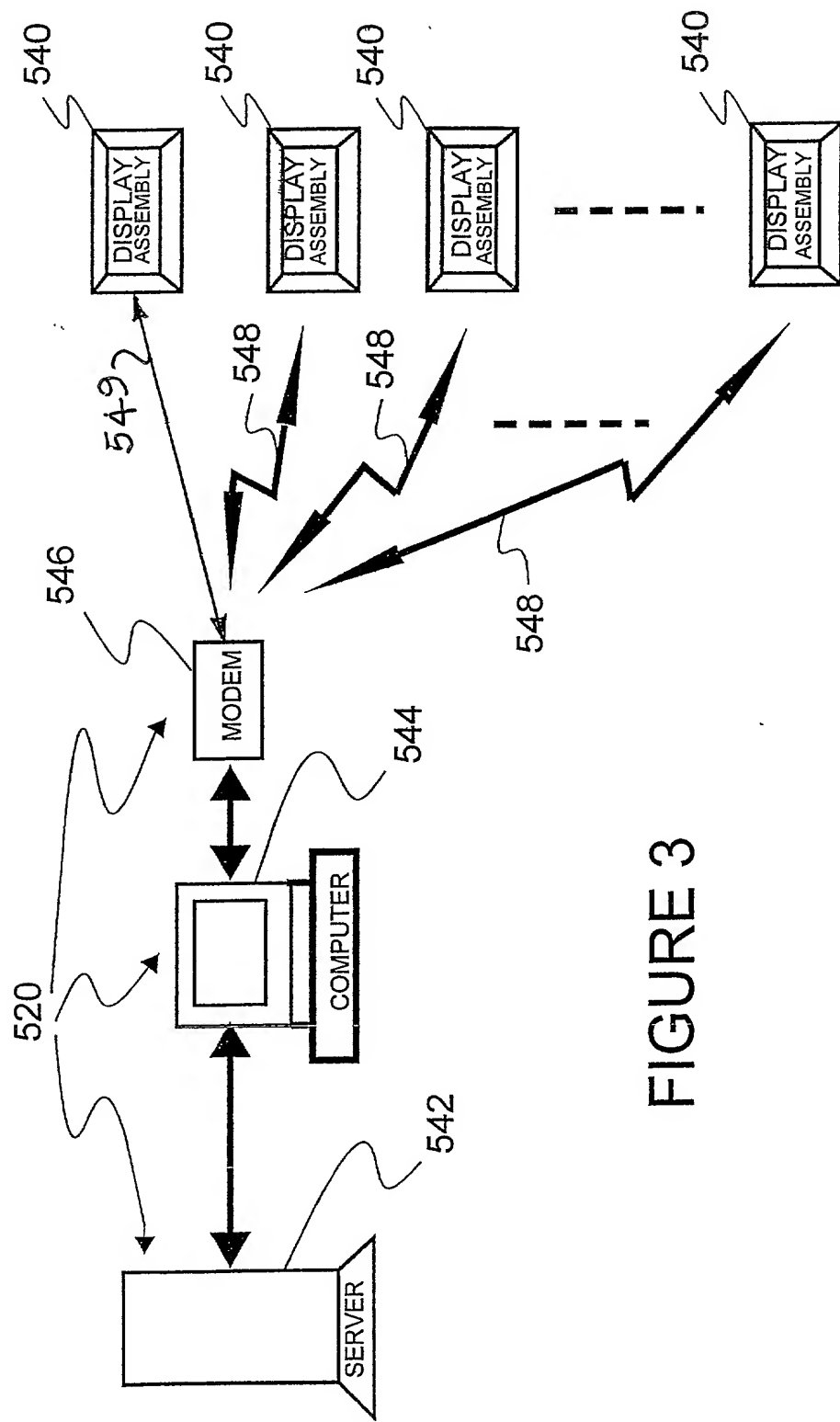


FIGURE 3

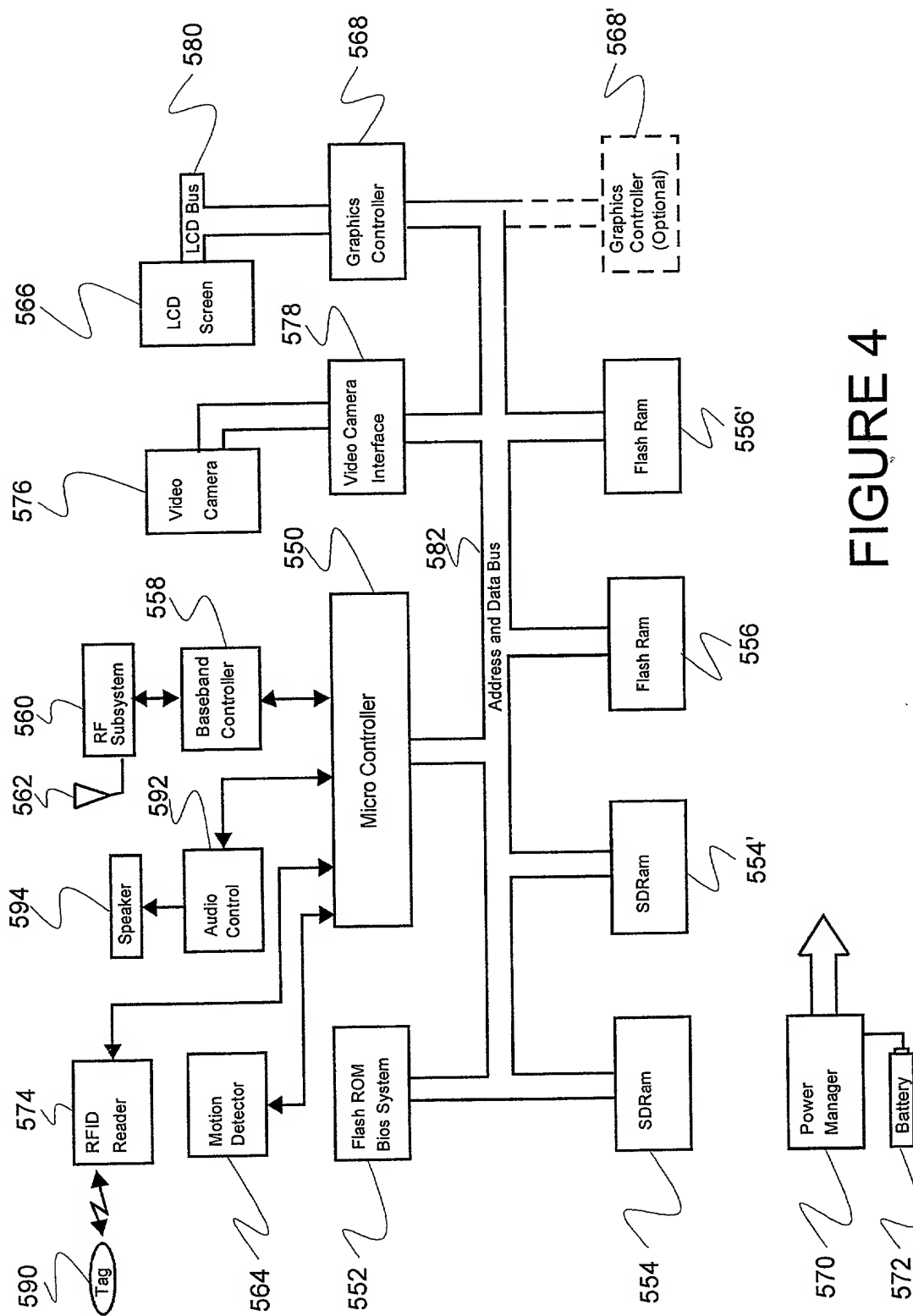


FIGURE 4

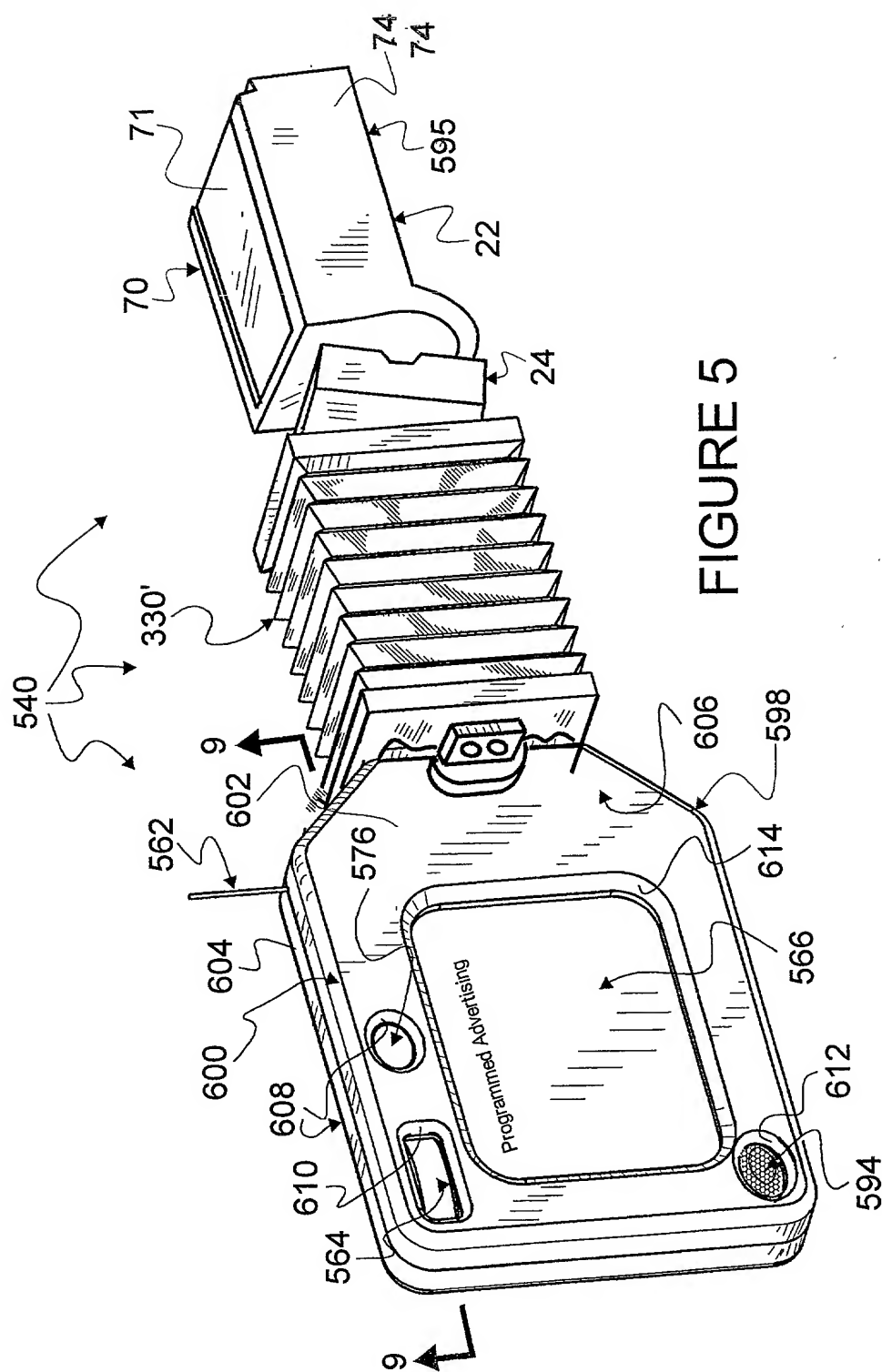


FIGURE 5

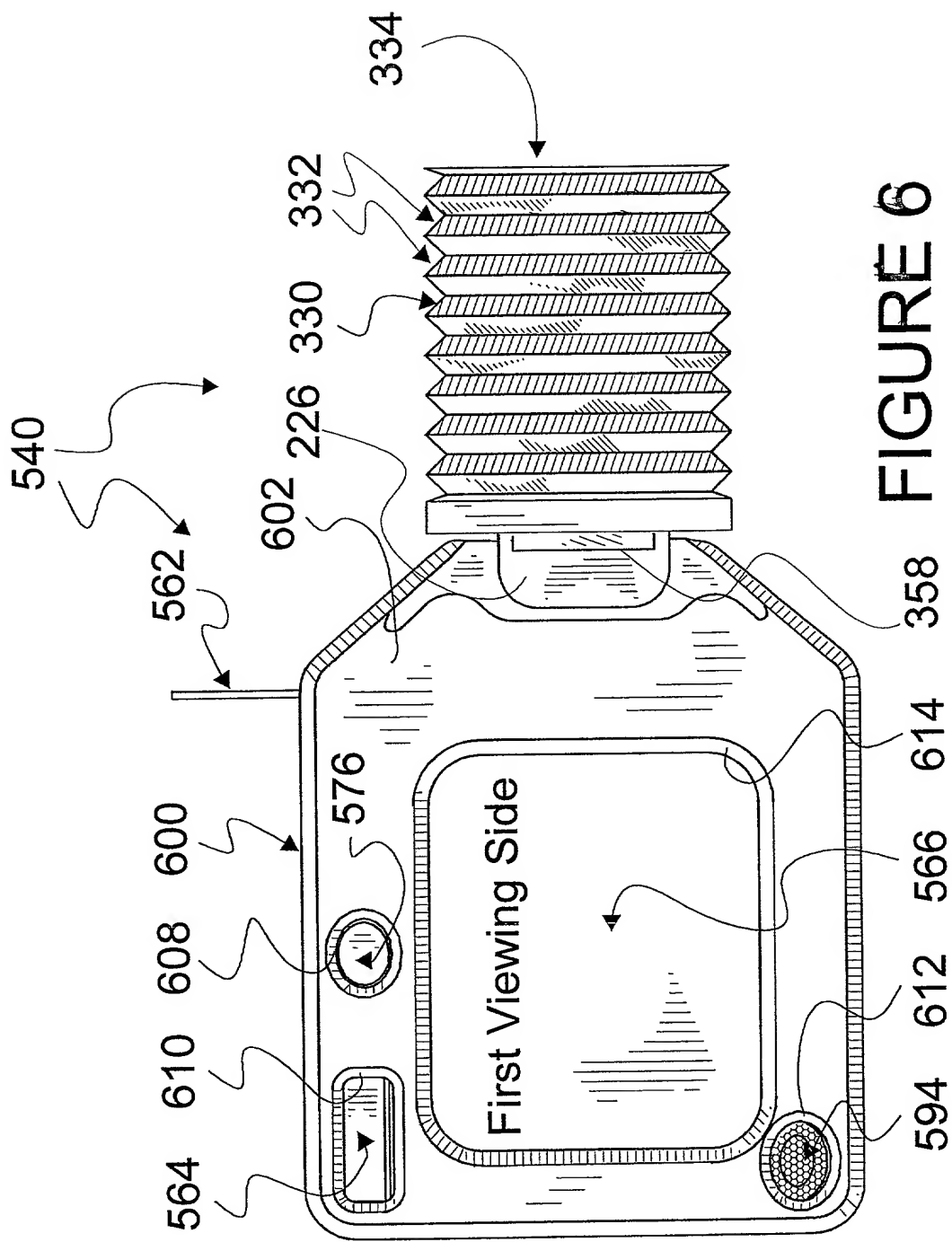
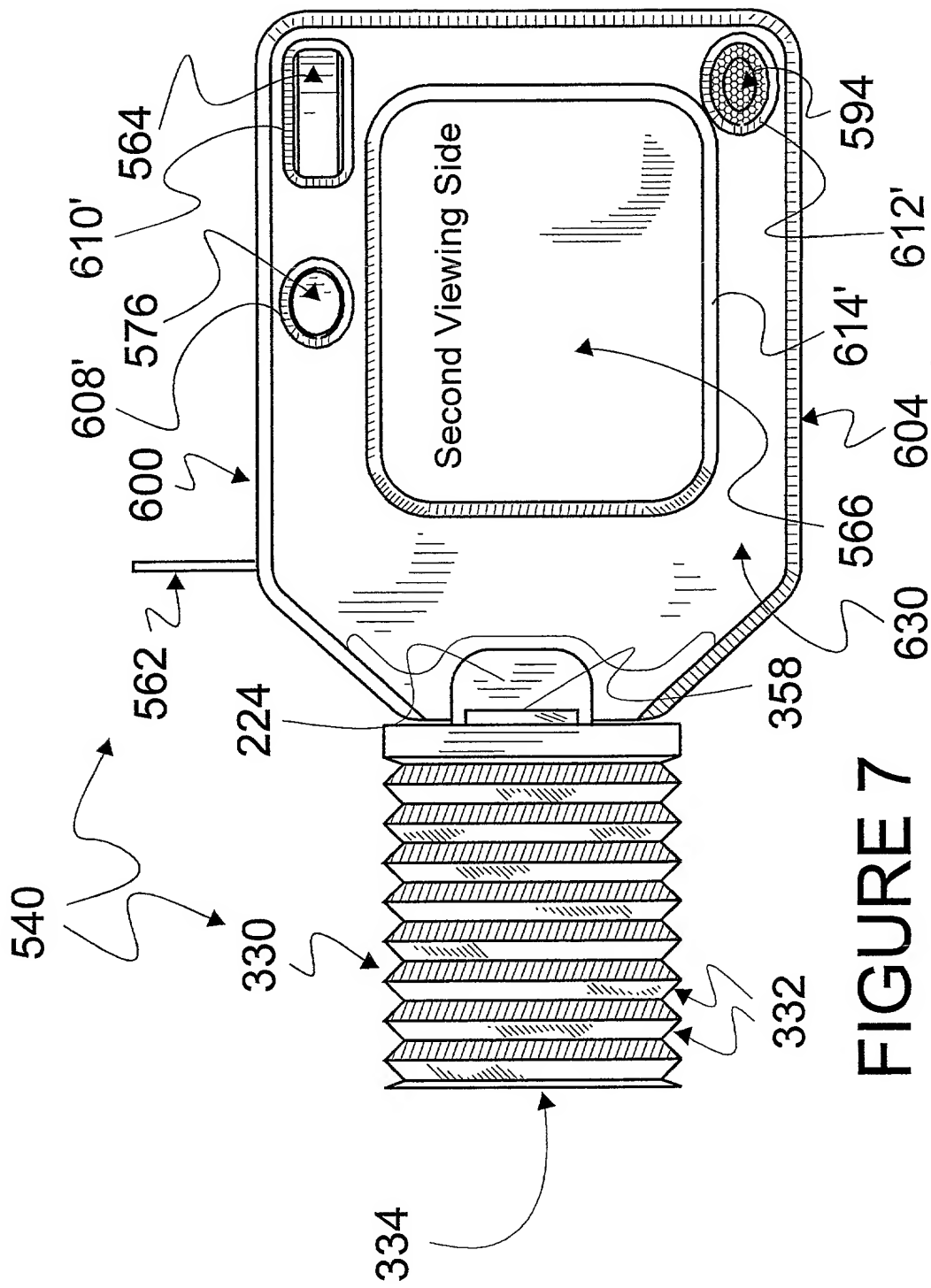


FIGURE 6



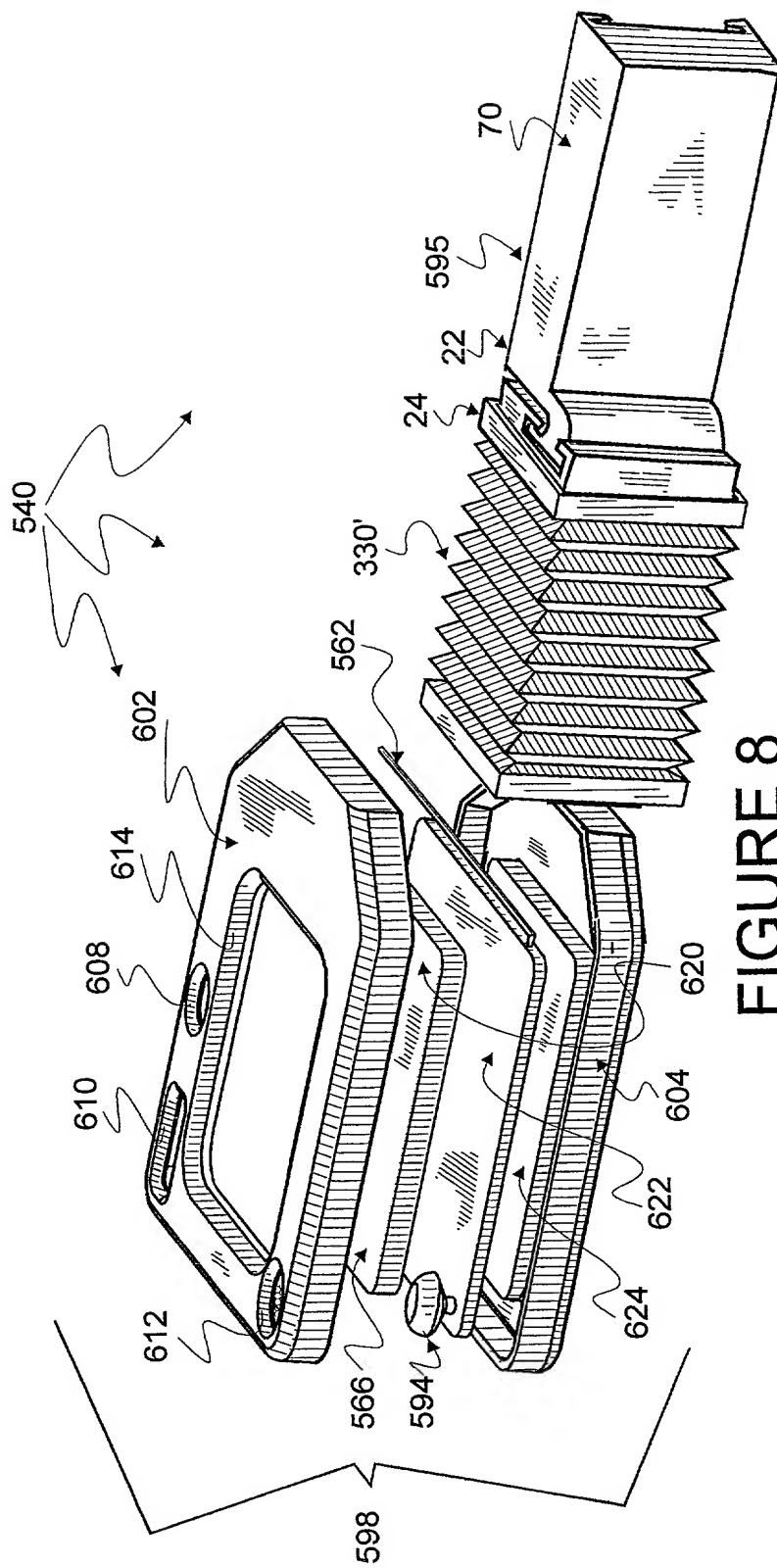


FIGURE 8

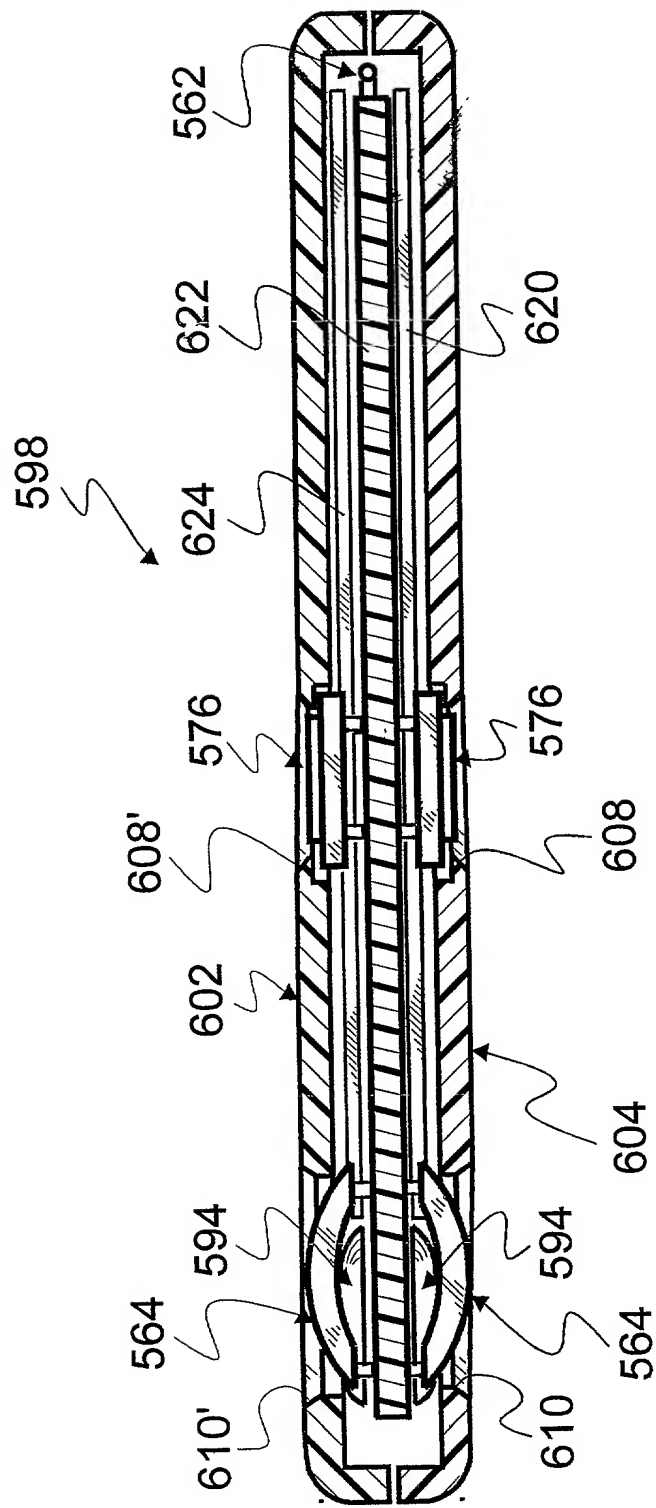


FIGURE 9

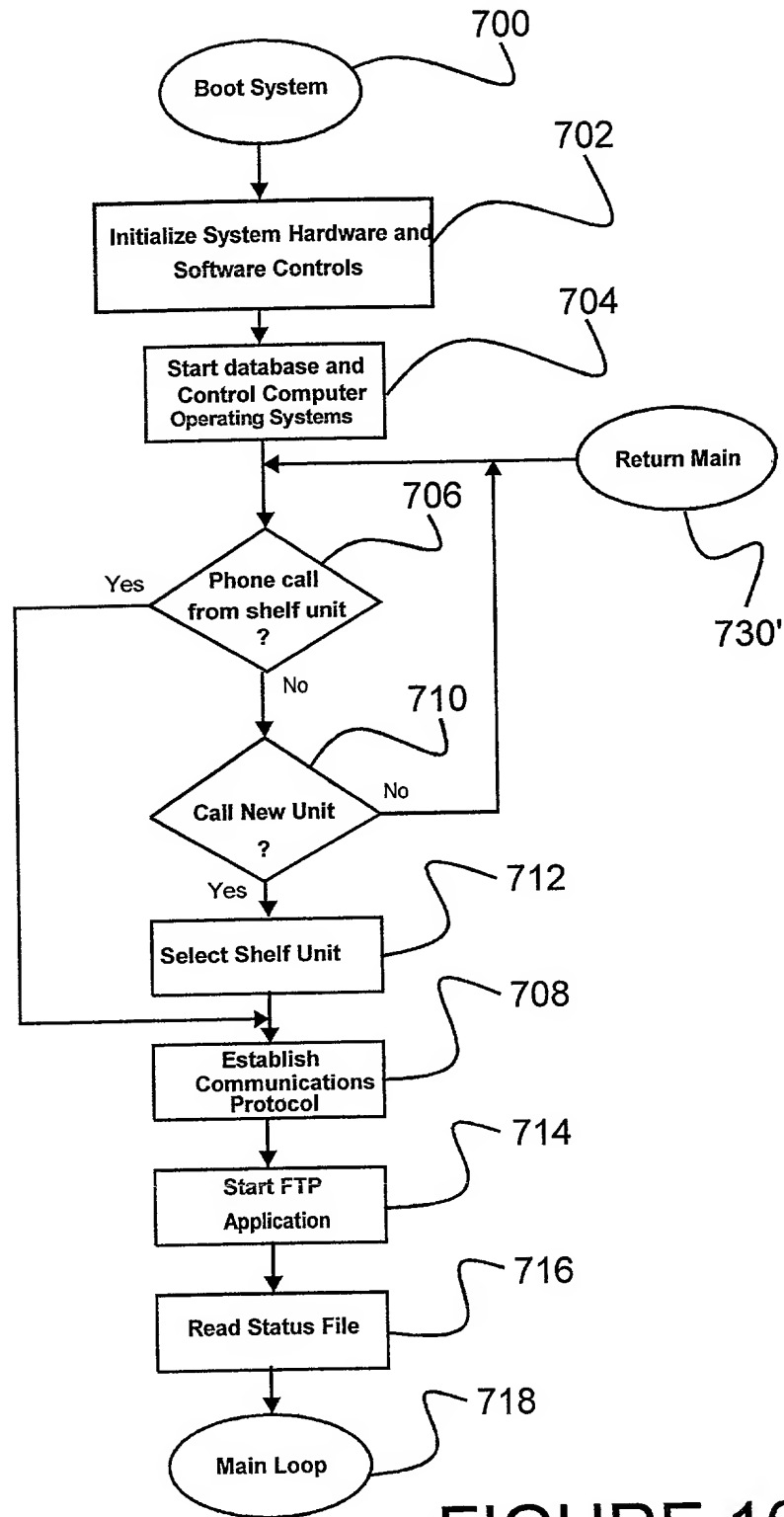


FIGURE 10

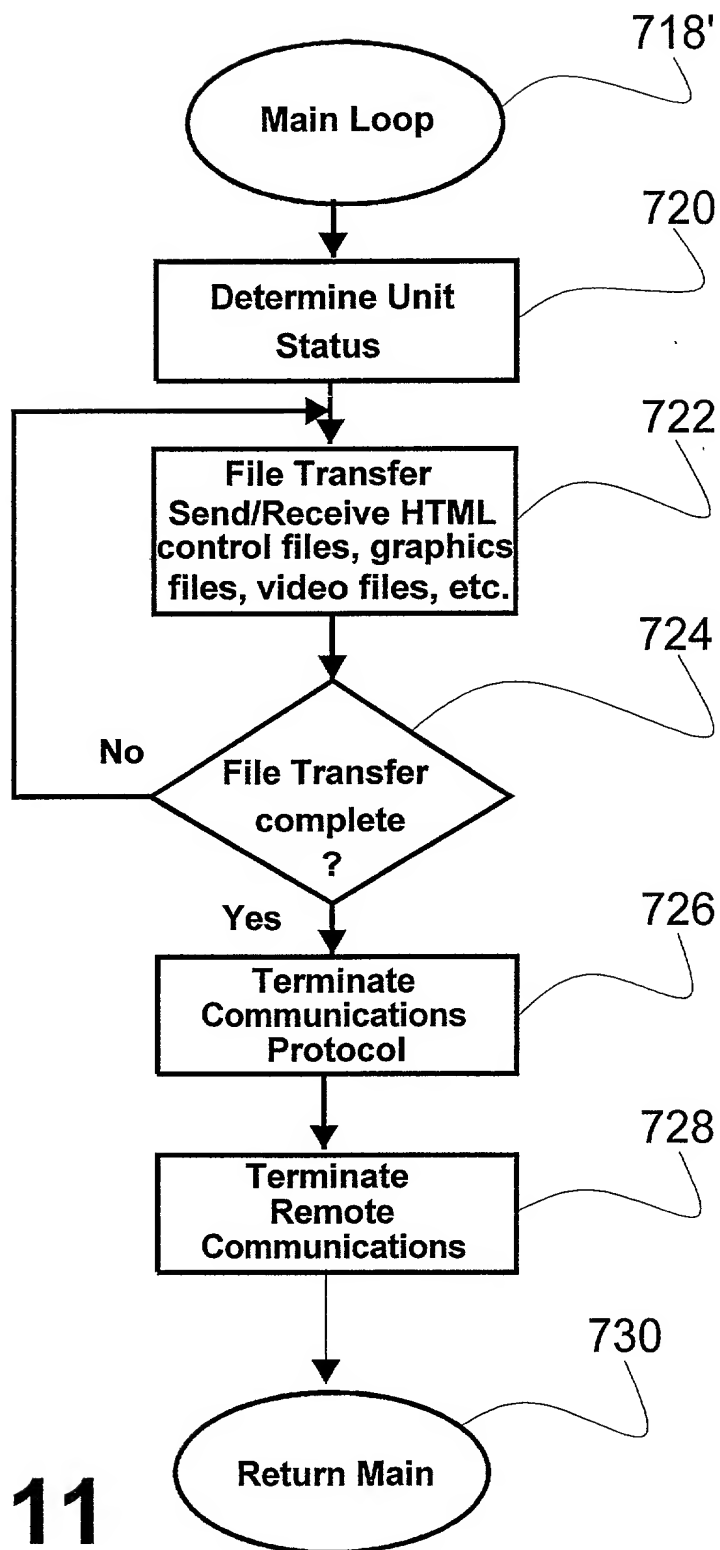
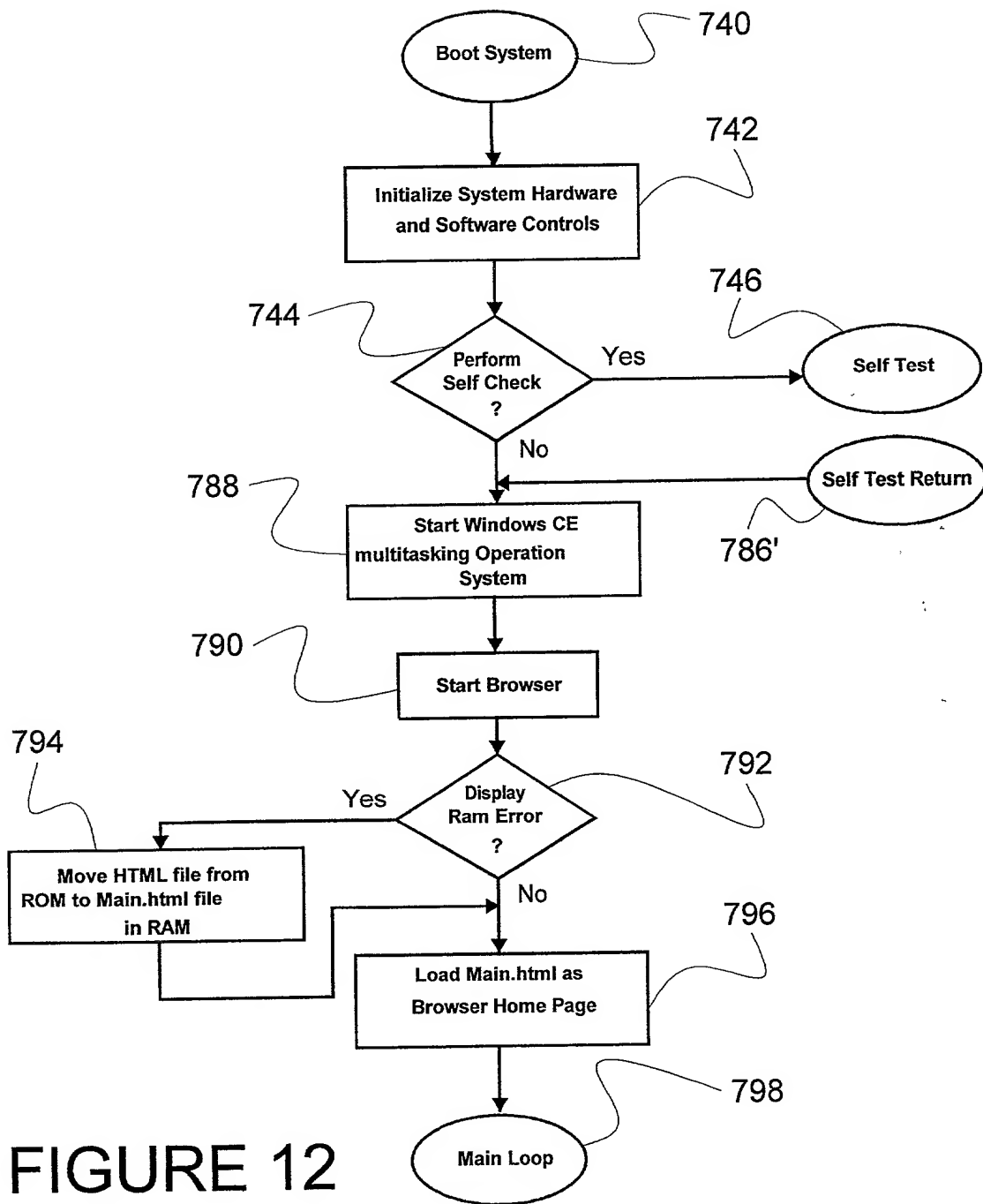


FIGURE 11



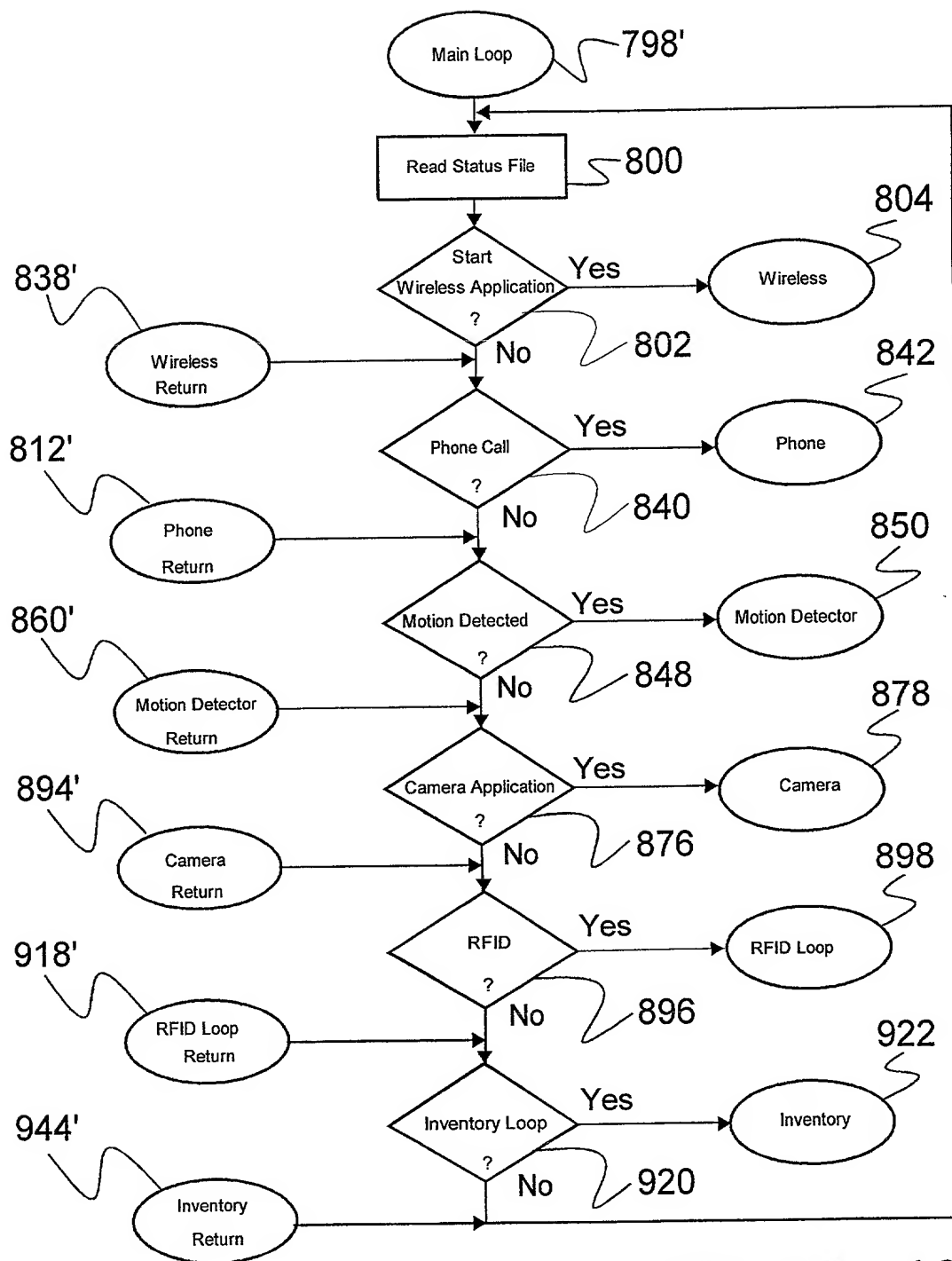


FIGURE 13

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graph TD
    746([Self Test]) --> 748[I/O Test]
    748 --> 750{Pass Test ?}
    750 -- No --> 752[Set I/O fail in Status File]
    752 --> 754[Battery Test]
    750 -- Yes --> 754
    754 --> 758{Pass Test ?}
    758 -- No --> 762[Set Battery Fail in Status File]
    762 --> 756[Test ROM Checksum]
    758 -- Yes --> 756
    756 --> 760{Pass Test ?}
    760 -- No --> 764[Set ROM fail in Status File]
    764 --> 766([Self Test 2])
    760 -- Yes --> 766
  
```

FIGURE 14

FIGURE 14

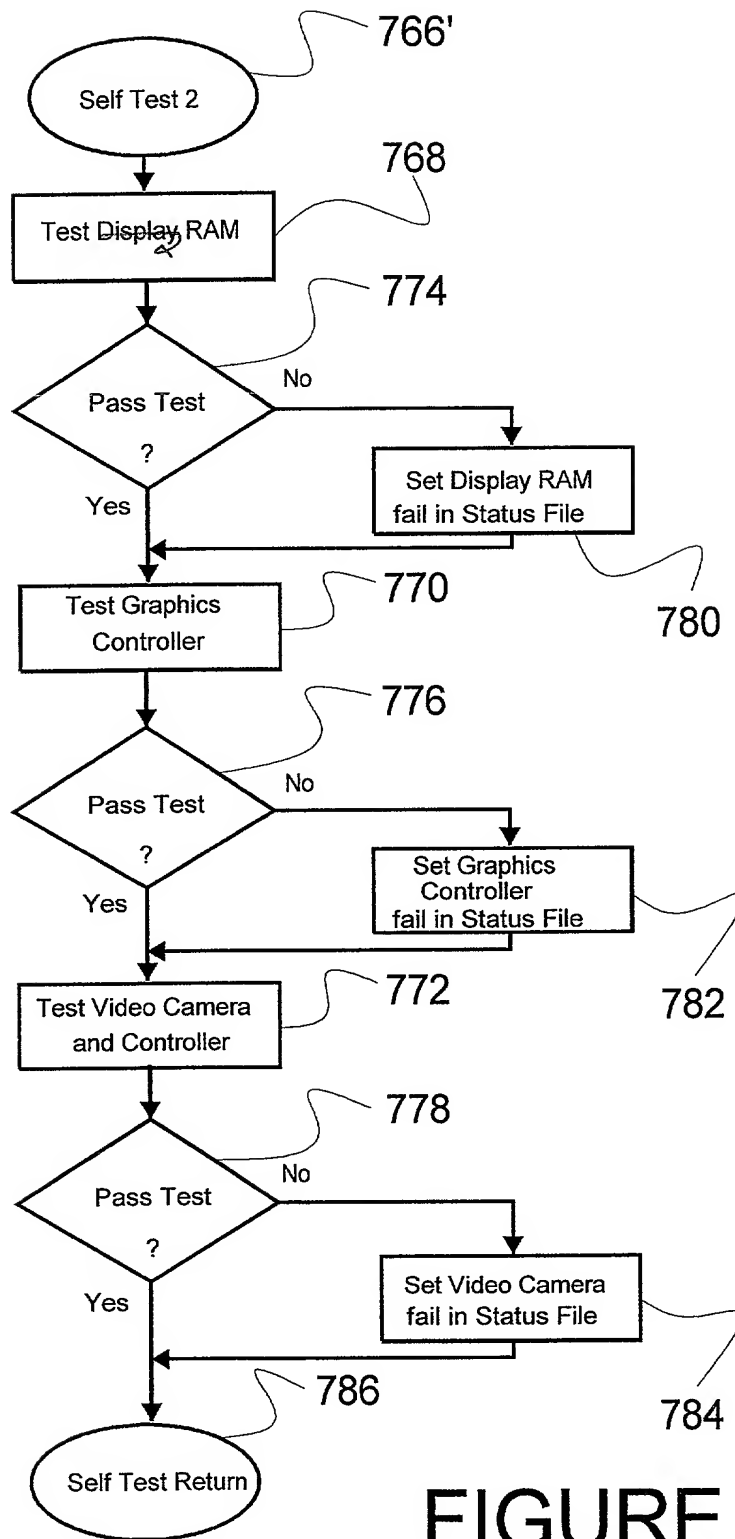
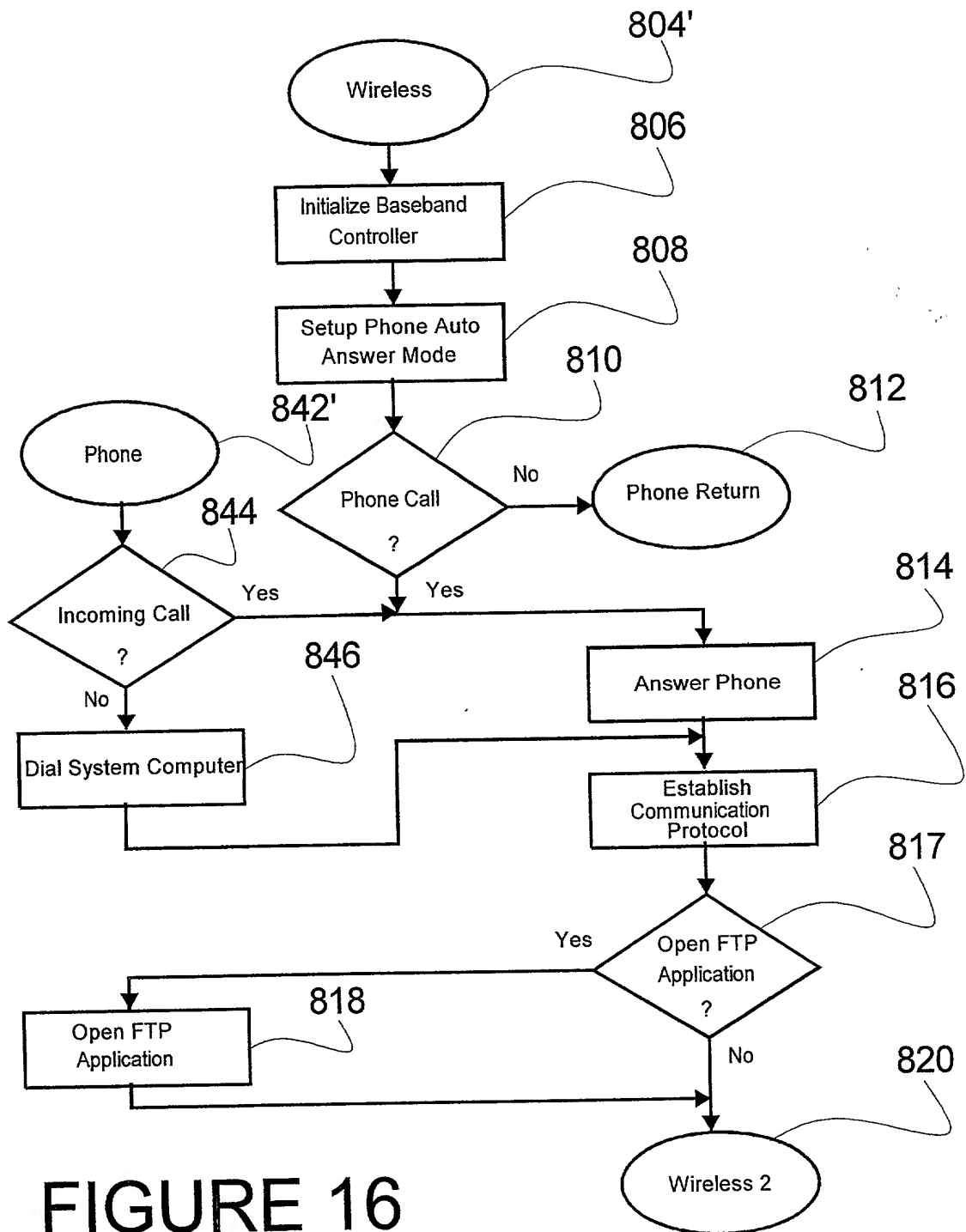


FIGURE 15



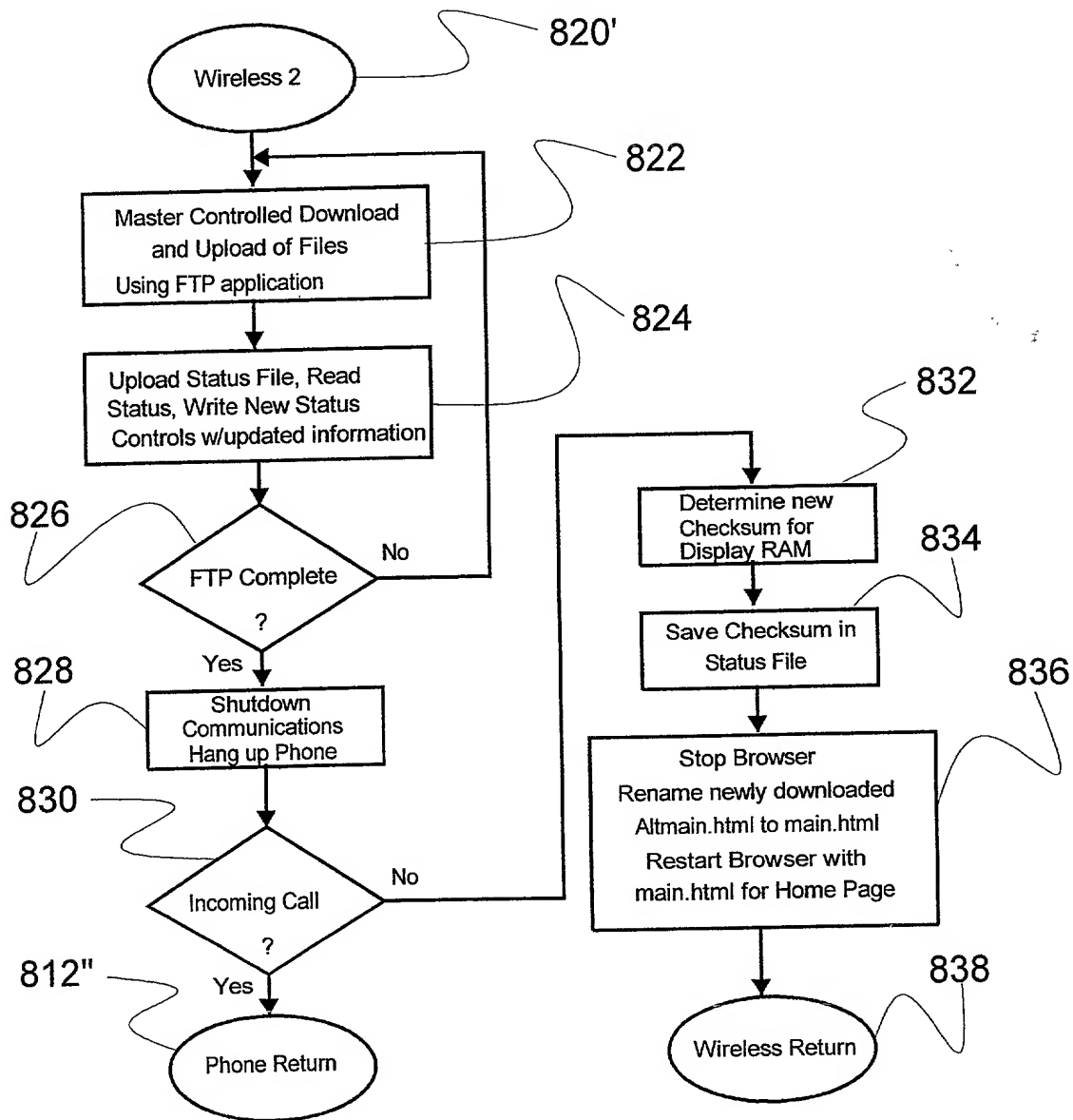


FIGURE 17

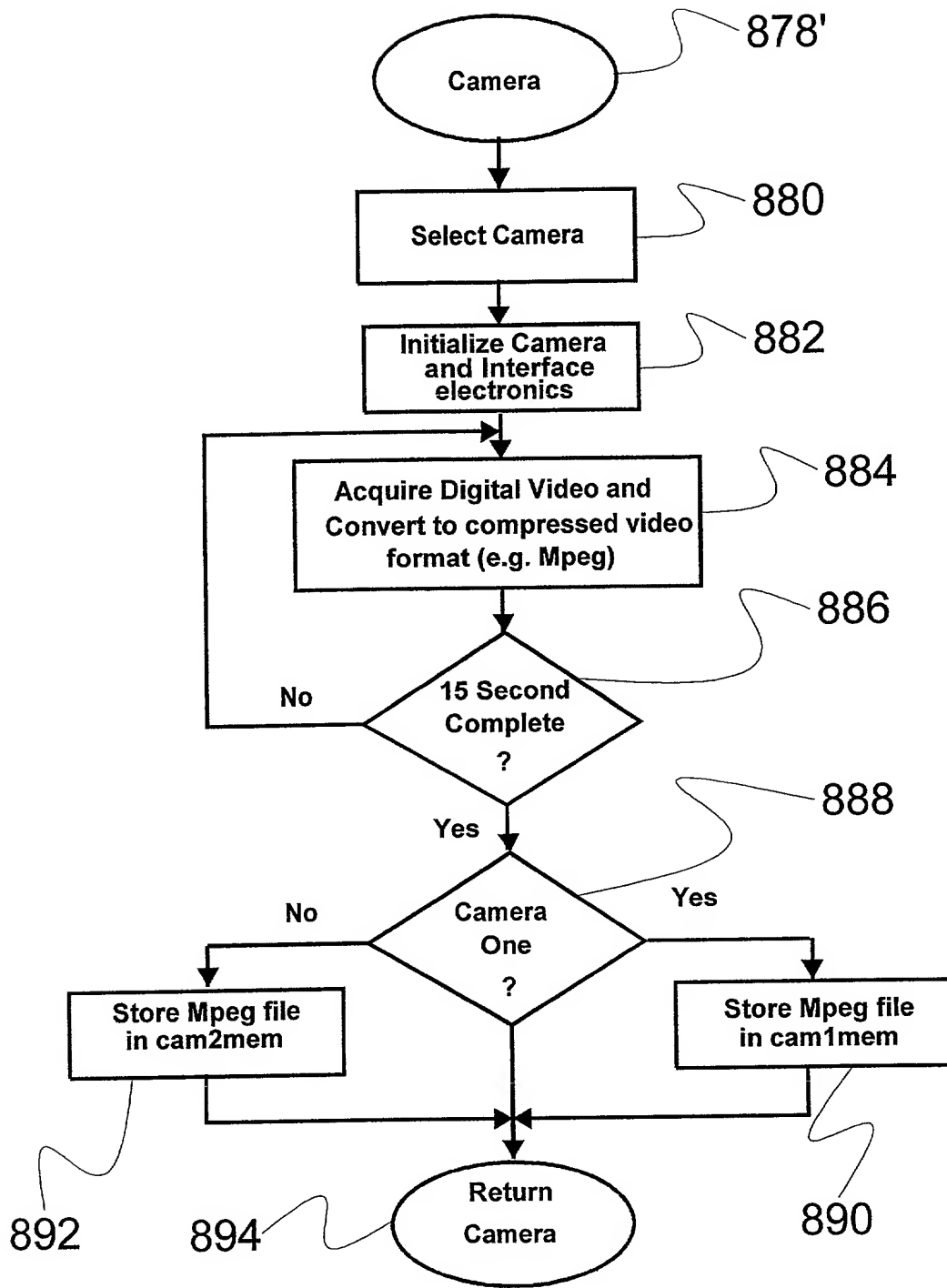


FIGURE 18

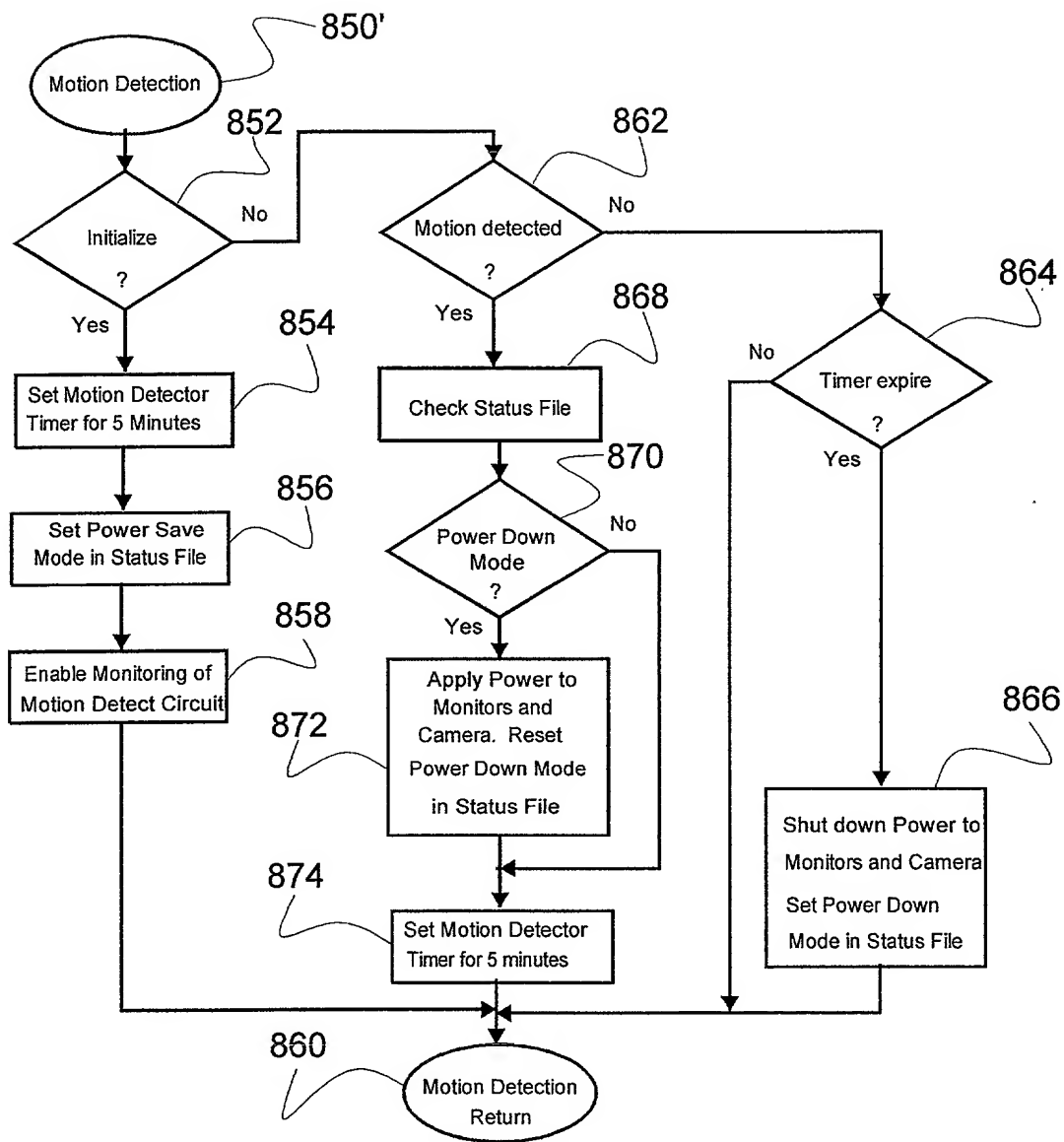


FIGURE 19

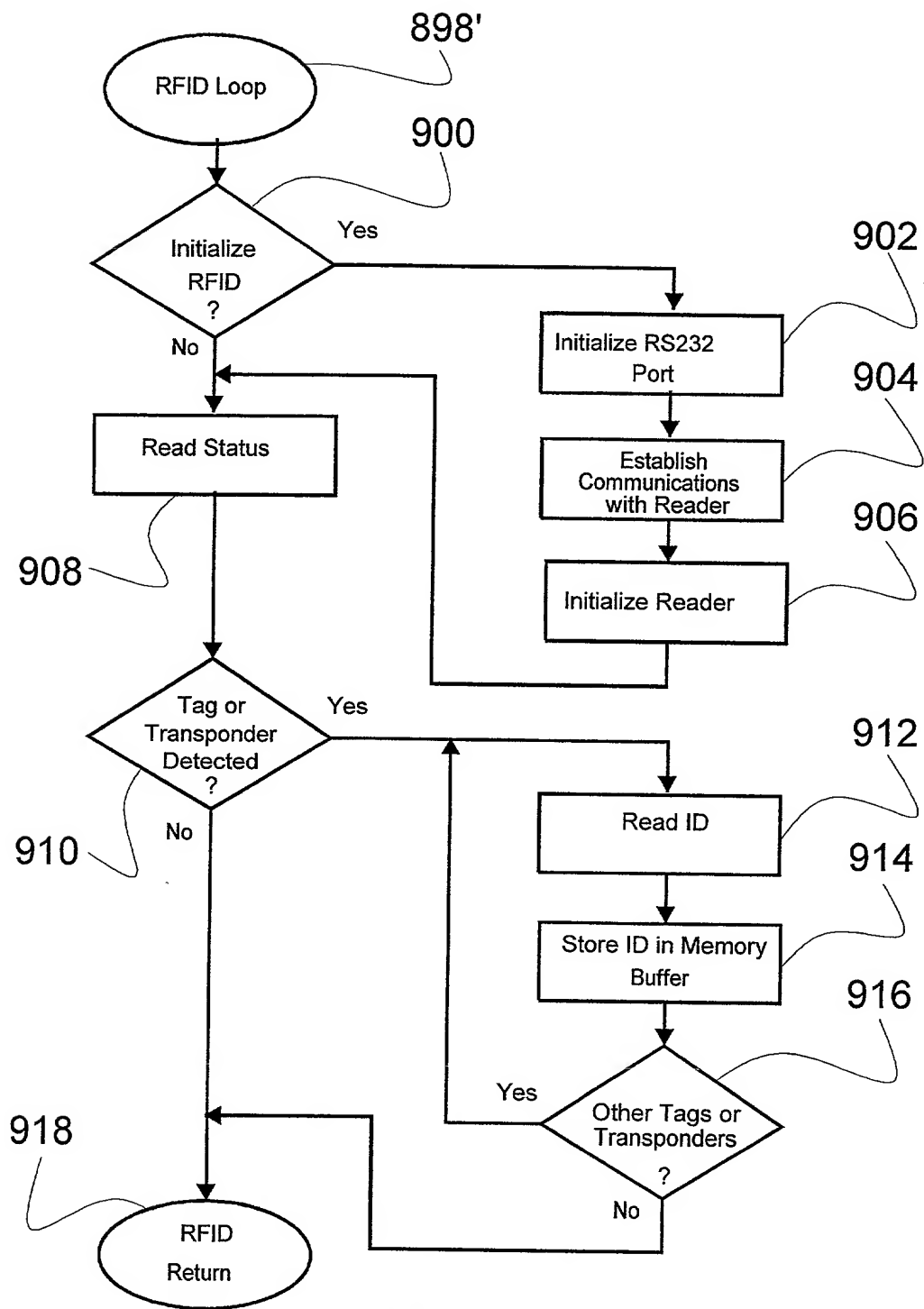


FIGURE 20

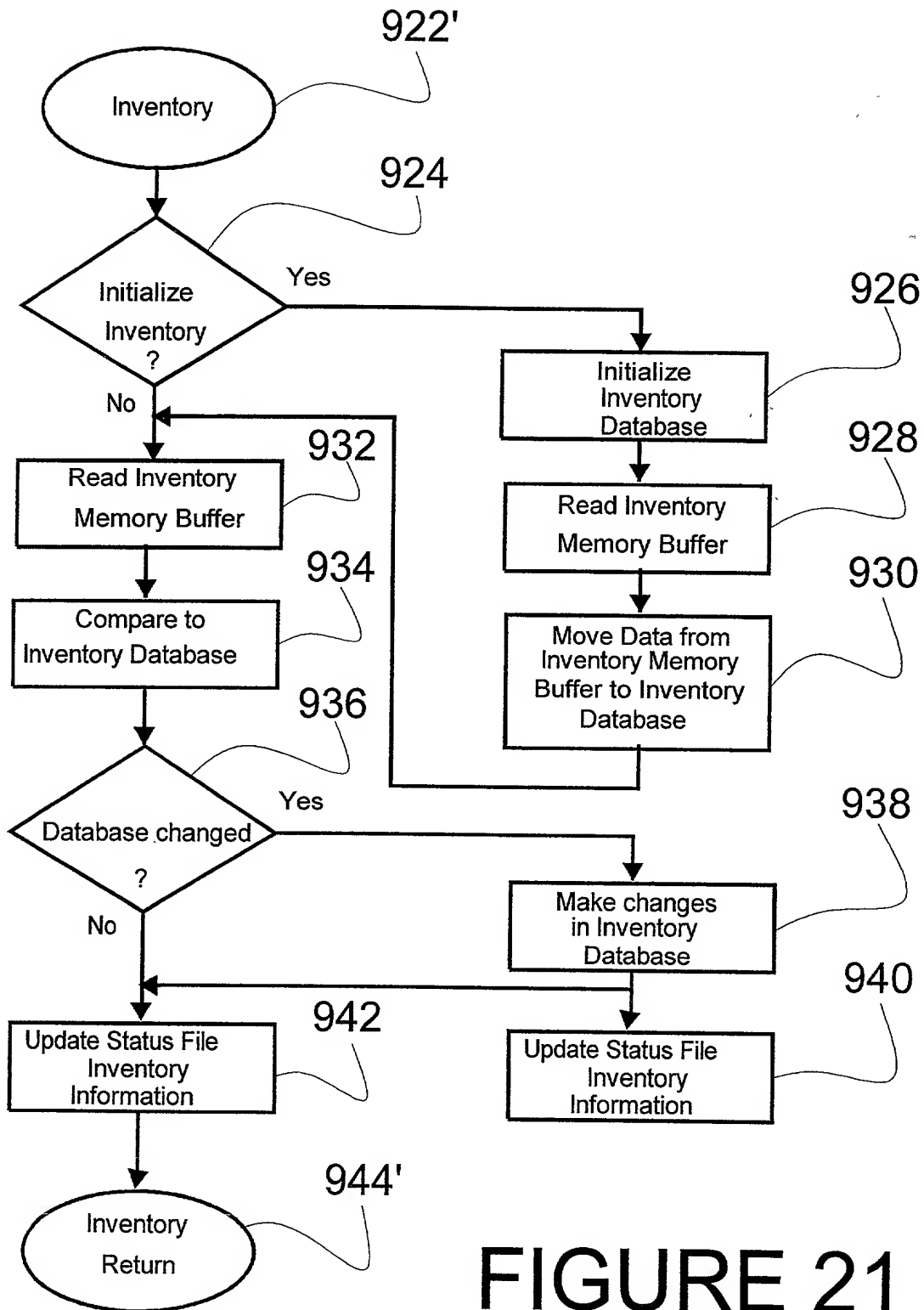


FIGURE 21